


## Faculty Profile

<b>Name of Faculty :</b> C. K. KALAWADE			
<b>Designation :</b> Assistant Professor			
<b>Department :</b> Electronics & Telecomm.			
<b>E Mail ID :</b> chaitanya.kalawade@gmail.com			
<b>Qualification:</b>	<b>UG</b>	<b>PG</b>	<b>Ph.D.</b>
	B.E. (Electronics)	M.E. (VLSI & Embedded Systems)	Nil
<b>Total experience in years :07</b>	<b>Teaching :</b> 06 <b>Industry :</b> 01		
<b>Papers published in Journals :01</b>	<b>National :</b> Nil <b>International :</b> 01		
<b>Papers published in Conferences :</b>	<b>National :</b> Nil <b>International :</b> Nil		
<b>Professional Memberships with number:</b>	Nil		
<b>Projects Guided :</b>	<b>UG</b>	<b>PG</b>	<b>Ph.D.</b>
	12		
<b>Books Published/IPRs/Patents :</b>	<b>Books :</b> Nil <b>IPR :</b> Nil <b>Patents :</b> Nil		
<b>Subject Expertise :</b>	Embedded System & RTOS Nanotechnology Data structure Operating System Concept		
<b>Consultancy Activities :</b>	Nil		
<b>Awards Received :</b>	Nil		
<b>Grants fetched :</b>	Nil		