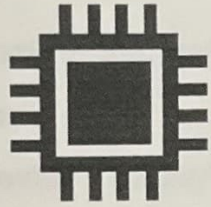




MEMORANDUM OF UNDERSTANDING (MOU) FOR ACADEMIC COLLABORATION

Between



TAKSHILA VLSI

**TAKSHILA INSTITUTE OF VLSI
TECHNOLOGIES,
BENGALURU, KARNATAKA**

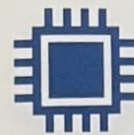
And



**DR. VITHALRAO VIKHE PATIL COLLEGE
OF ENGINEERING,
AHMEDNAGAR - 414111, MAHARASHTRA**

This MOU is entered onto on the 14th day of October 2023 by and between Takshila Institute of VLSI Technologies (hereafter called Takshila VLSI) situated at 2nd Floor, Ram Arcade, 90/1B, CKB Layout, Chandra Layout, Marathahalli, Bengaluru – 560037, Karnataka.





and

Dr. Vithalrao Vikhe Patil College of Engineering, (hereafter called College)
situated at Ahmednagar, Maharashtra State

1. Objectives of MOU

- a. To promote and enhance semiconductor / VLSI domain interest in students.
- b. To provide Online/Offline/Weekend training to students in VLSI courses such as Physical Design / Analog Layout /ASIC Verification / Design For Test (DFT).
- c. To provide internship to students from VLSI Service provider company SiValley Technologies Pvt. Ltd. in Digital or Analog Domains.
- d. To provide placement assistance to the students who complete the course / internship.
- e. To provide academic interaction by delivering special lectures at college on topics of relevance to semiconductor domain

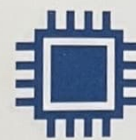
2. Proposed Modes of Collaboration

Takshila VLSI and College to collaborate through the following:

- a. Cooperation and promotion of education and training in areas of mutual interest.
- b. Any other appropriate mode of interaction agreed upon between Takshila VLSI & College.

Any specific plan will be worked out by the college depending upon availability of resources.





3. Terms and Conditions

- a. For the visits related to advice & consultancy, travel and other expenses of Takshila VLSI staff shall be reimbursed by College on mutually agreed terms.
- b. Usage of College academic infrastructure can be allowed for limited period subject to its availability, approval of Head of the facility/ department and college norms.
- c. This MOU may be amended, renewed and terminated by mutual written agreement of the *colleges* at any time.
- d. Either Takshila VLSI or College shall have the right to terminate this MOU upon 60 days prior written notice to the other.

4. Confidentiality

The Takshila VLSI and the College agree to hold in confidence all information /data which is obtained from either *Takshila VLSI or College* or created during the performance of the MOU and will not disclose the same to any third party without written consent of the other.

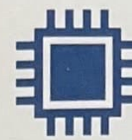
5. Duration of MOU

This MOU, unless extended by mutual written consent of the *both*, shall expire in FIVE years after the effective date. However, on review, the MOU shall be extended for another TWO years by mutual consent.

6. Coordinators

Both Takshila VLSI and College will designate persons who will have responsibility for coordination and implementation of this agreement.




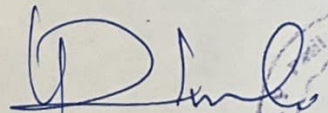

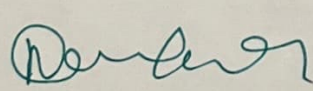
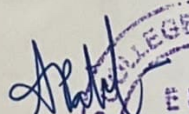

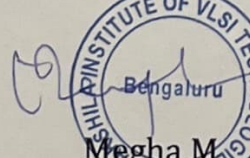
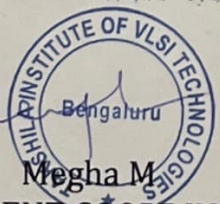
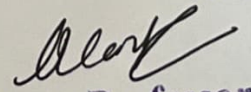


7. Intellectual Property Rights

Intellectual Property Rights (IPR) that arise as a result of joint research and collaborative activity under the agreement will be worked out on a case to case basis and will be consistent with officially laid down IPR policies both.

8. Signed in Duplicate

The MOU is executed in duplicate with each copy being an official version and having equal legal validity. By signing below, the *Takshila VLSI and College*, acting by their duly authorized officers, have caused this Memorandum of Understanding to be executed, effective as of the day and year first above written.

On behalf of TAKSHILA INSTITUTE OF VLSI TECHNOLOGIES <i>For Takshila Institute of VLSI Technologies</i>  <i>Authorized Signatory</i> Venkataswamy Nandam DIRECTOR	On behalf of DR. VITHALRAO VIKHE PATIL COLLEGE OF ENGINEERING  Dr. U.P. Naik PRINCIPAL 
 Naveen Kumar Operations Manager	 Dr. Anita K Patil HOD (ECE) Dept. 
 Megha M PLACEMENT COORDINATOR 	 Prof. A.R. Landge PLACEMENT COORDINATOR 