

|| न हि ज्ञानेन सदृशं पवित्रमिह विद्यते || Dr. Vitthalrao Vikhe Patil Foundation's

Dr. Vithalrao Vikhe Patil College of Engineering Ahmednagar DTE College Code: EN-5161



Department of Electronics and Telecommunication Activities of Department Association EESA for A.Y.2016-17

Sr. No.	Name of the Event	Date(s)	No. of Participants	Level of Event
1	Farewell	3 April 2017 (6 th April)	162	Institute Level
2.	Career Opportunities in Electronics &Telecomm by Mr.AshutoshAthawale	11 th March 2017	92	Institute Level
3.	Coding (C/C++) Competition	12 Feb 2017	38	Institute Level
4.	Predictive Analytic Automotive Industry by Dhananjay Banger, Einfochip ,Pune	19 th Jan 2017	84	Institute Level
5.	Expert Lecture on "Entrepreneurship in the field of Robotics" Mr. YuvrajPardeshi, Director operations, Yushai Systems Shreyash Industries, MIDC, PimpriChinchwad, Pune	25 th Oct 2016	92	Institute Level
6.	Study of PCB for project work by ArvindKumawat,ApplicationManagerCTR Manufacturing plant, Nashik	15 th Oct 2016	114	Institute Level



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Department of Electronics and Telecommunication Activities of Department Association EESA for A.Y.2017-18

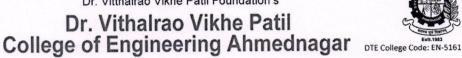
Sr. No.	Name of the Event	Date(s)	No. of Participants	Level of Event
1	Farewell Institute Level	09 April 2018	135	Institute Level
2.	Group Discussion	24 Jan 2018	13	Institute Level
3.	Project Competition	12 Jan 2018	38	Institute Level
4.	Poster Presentation Competition	11 Jan 2018	60	Institute Level
5.	Seminar on "Scope of Nanotechnology" (Engineer's Day)	15 sept 2017	97	Institute Level
6.	Quiz Competition	15 sept 2017	95	Institute Level
7.	Teachers day	5 Sep 2017	40	Institute
	Tree Plantation	5 Sep 2017		Level



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Department of Electronics and Telecommunication Activities of Department Association EESA for A.Y.2018-19

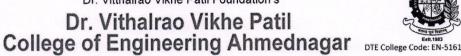
Sr. No.	Name of the Event	Date(s)	No. of Participants	Staff Coordinator
1	Farewell	9 May2019	95	Prof.V.A.Dhawale
2.	3 days Workshop on Embedded System, IOT and Ardiuno by Mr.Akash Dane, and Mr.SumedhSaptarshi	21 to 23 Feb 2019	39	Prof.V.A.Dhawale
3.	Expert Lecture on the occasion of Engineer's week 2018	21-Sep-18	38	Prof.V.A.Dhawale
4.	Teacher's Day	5-Sep-18	74	Prof.V.A.Dhawale
5.	Quiz Competition	5-Sep-18	95	Prof. A.R. Mane



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Department of Electronics and Telecommunication Activities of Department Association EESA for A.Y.2019-20

Sr. No.	Name of the Event	Date(s)	No. of Participants	Staff Coordinator
1.	Aptitude Test	26 th May 2020	228	Prof.P.P.Bhalerao
2.	Quiz in C & C++	24 th May 2020	183	Prof.J.P.Botkar
3.	Quiz Competition	23 rd May to 1 st June 2020	226	Prof.A.R.Landge
4.	Quiz Competition	21 st May 2020 to 25 th May 2020	387	Prof.S.R.Udavant
5.	Paper Presentation	12 th Feb 2020	27	Prof. V.A.Dhawale
6.	Project Competition	12 th Feb 2020	9	Prof. V.A.Dhawale
7.	Coding (C/C++) Competition	12 th Feb 2020	17	Prof.P.B.Varpe Prof.A.U.Vikhe
8.	Poster Competition	11 th Feb 2020	76	Prof. V.A.Dhawale
9.	Logo making Competition	11 th Feb 2020	33	Prof. V.A.Dhawale
10.	Gaming Competition	11 th Feb 2020	95	Prof. V.A.Dhawale
11.	Tree Plantation	19 th July 2019	120	Prof. V.A.Dhawale







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Department of Electronics and Telecommunication EESA

Organised

E-Technival 2K21(State Level Competition)23rd May 2021

Registration Link:

https://docs.google.com/forms/d/e/1FAIpQLScXIO9257l697JUzxBvDTmTvbelb3ZbGSYZZ B Vr1tEWGA3Pw/viewform?usp=sf link

Competitions:

- 1. Electoon
- 2. Circuit Control
- 3. Engineer Meets Poetry
- 4. Crossword

Students Attended: From Engineering and Diploma colleges of different places in Maharashtra.

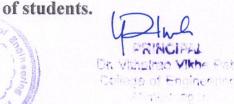
No. of students participated:-140

Event In charge: - Prof. J.P. Botkar **Student Co-ordinators:**

- 1. Mr.Rohit Rahinj B.E.E&TC,
- 2. Mr. Yash Patole (T.E.E&TC)
- 3. Miss.Kalyani Gadekar (T.E.E&TC)
- 4. Mr.Ainual Bagwan (T.E.E&TC)
- 5. Mr.Sanket Nirwane (T.E.E&TC)

Objective: -

- 1. To develop Logical Thinking
- 2. To improve Circuit analysis and design ability of students.
- 3. To provide platform for Artistic nature of student in technical environment.
- 4. To improve the technical vocabulary of students.





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Outcomes

- 1. Students will be able to think in different way for same problem.
- 2. Students will be able to design the circuit according to the problem given.
- 3. Students will get platform for their artistic thinking.
- 4. Students will be able to give more words for different technical definitions.

CO-PO Manning

CO	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	P012
CO1								2	2	2		
CO ₂			3									
CO3						2		3	3			
CO4				2				3	3			

CO-PSO Manning

CO	PSO1	PSO2	PSO3
CO1			
CO2	3	-	14
CO3			
CO4			



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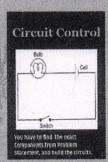


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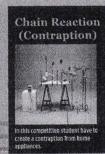
(Electronics & Telecommunication Engineering Student's Association) Organizing

STATE LEVEL E-TECHNIVAL 2K21











General Instruction :-

1. Each Participant Will Get E- Certificates and Win Cash Prizes. 2.No Entry Fees

3.Must Fill The Google Form For The Competition. 4.For More Details Scan The QR Code

Register for competition till 20 May 2021. Choose your choice of competition and upload your work on given link till respective date.

Contact Us: 8830193132/8888438955

R.Z.Rahinj President EESA Commitee Prof.J.P.Botkar EESA Coordii E&TC Departi

Dr.A.K.Patil

Dr.U.P.Naik

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Department of Electronics & Telecommunication Engineering

EESA

(Electronics & Telecommunication Engineering Student's Association) Organizing

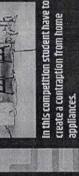
STATE LEVEL E-TECHNIVAL 2K21



Circuit Control

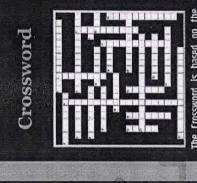
Electoon

3



engineering and technology.

Components from Problem You have to find the exact Switch



The Crossword is based on the



Electronic theme. Draw the sketch

Jake a cartoon based on

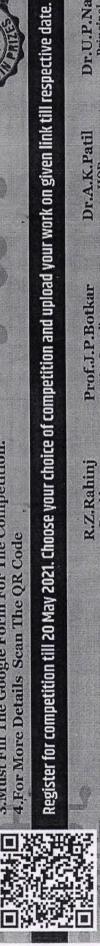
College

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1. Each Participant Will Get E- Certificates and Win Cash Prizes.

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EFSA Committee R.Z.Rahinj

Prof.J.P.Botkar EESA Coordinator E&TC Department

E&TC Department Dr.A.K.Patil

Principal DVVP COEA

Contact Us: 8830193132/8888438955

Dr.U.P.Naik

Dr. Vitthalrao Vikhe Patil College of Engineering, Ahmednagar Department of Electronics and Telecommunication Engineering

1 - 2 - 27

Office Order

Date: 22/05/21

STATE LEVEL E-TECHNIVAL 2K21

Following staff members are appointed as Judge for the events of E-Technival2K21. Details are as follows

Competition	Student Coordinator	Judges
Poem-	Yash Kumbhakarn (T.E.) Rushikesh Rohit Ballal (S.E.)	 ▶ Prof. N.M.Deshpande ▶ Prof.A.R.Udawant ▶ Prof.A.U.Vikhe
Electoon	Sarika Pardeshi (T.E.) Shagun Sharma (T.E.)	➢ Prof.P.P.Bhalerao➢ Prof.A.R.Landge➢ ProfR.J.Lawande

Dr. Anita K.Patil H.O.D. E&TC Dept.

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Department of Electronics and Telecommunication Engineering Dr. Vittha. o Vikhe Patil College of Engineering, Ahr. Juagar

Online Prize Distribution

Date:15/6/2021

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	Transaction	by online payment			La della como	1 - 10 1		1					
	Amount	Rs.	300/- ^-	200/-	300/- 💉	200/-	300/-	200/- Phone pay	100/-	300/-	200/- V	2100/-	Dr.A.K.Patil
2K21	Phone (account	number)	9370609672 Brother account	88560 41080	9767415897	9657671644	8805314779	8830374212	7620153013 (Shradha shinde)	72767 71445	7620153013	Total	80
E-TECHNIVAL 2	Prize		Winner	Runner up	Winner	Runner-up	Winner	1st runner up	2 nd runner-up	Winner	Runner-up		
LEVEL E-TEC	College		SIT Plytechnic, Yadrav, Kolhapur	D.Y.Patil College of Engineering, Pune	Late G N Sapkal Knowledge Hub College Of Engineering	NDMVP college of engineering, nashik	PICT , Pune	Dr.V.V.P.CoE Ahmednagar	Government polytechnic a.nagar	Dr.V.V.P.CoE Ahmednagar	Government polytechnic a.nagar		RINCIPAL
STATE LE	Year/Class		S.E.E&TC	T.E.E&TC	B.E. E&TC	S.E.	B.E. E&TC	T.E.IT	T.E. Mech	B.E. E&TC	T.E. E&TC	4	College of
	Name of	student	Ankit Sing	Diya Karkhanis	Kamini Sakhahari Mundhe	Esha Manik Jadhav	Charvi Dilip Bhave	khose swapnil ramesh	Shendkar priti mahesh	Myana Vrushali Dnyaneshwar	Shraddha Sanjay Shinde		
	Competition		Engineer Meet Poem	, 1	Electoon	- 75	Crossword			Circuit Control		4	Prof. J.P.Botkar
	Sr.	No.	1.		2.		3.			4.			Prof.

EESA Coordinator Prof. J.P.Botkar

Dr. Vithalrao Vikhe Patii College of Engineering Ahmednagar RINCIPAL

HOD E&TC

Questions for Circuit control Competition

Department of Electronics and Telecommunication

EESA

organised

E-Technival 2K21

State Level Competition

23 rd May 2021
Q. A circuit of an amplifier gives high input impedance in order to have a proper signal measurement from the transducer. (hint: temperature controller)
Q. The circuit lends itself to use in small driver chips having same priority. (hint : use only 2 transistors from 3)
Q. It is a one kind of the circuit & the configuration of this circuit is based upon the standard inverting op amp.
Q. The circuit which cannot be used as low frequency, the harmonic content in the output of this circuit is very high. (hint: phase difference 360* & use only transistor)
Q. Draw the circuit diagram from given waveforms, When the input sine wave cross zero & become positive at instant t = 0, the differential input voltage become negative & the output. Given waveform -
Q. Draw the circuit such a way that it will operates in two mode. In 1 st mode the middle section can acts as a closed switch & output is sampled & in 2 nd mode that middle section act as an open switch that gives the constant no output. (hint: use two buffers)
Q. Draw the circuit which gives zero output for high input & high output for zero input using the technology which is better than BJT.
Q. Realize the following equation using NAND gate. 1. $Y = (A + B)(B + C)$ 2. $Y = AB + C$

Q. Draw the circuit which output is proportional in term of logarithmic of input.



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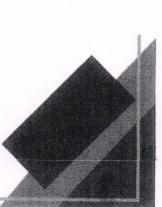
Department of Electronics & Telecommunication Department of Electronics & El

EESA

Electronics & Telecommunication Engineering Students Association

STATE LEVEL E-TECHNIVAL 2K21

category Contraption, dated on 23rd May 2021 at Dr. Vithalrao Vibhe Patil College of Studying in Mechanical has actively RUNNER UP in E-Technival State Level Competition, This is to certify that Mr/Ms Shendekar Priti Mahesh from Government Poly. A. nagar Engineering Ahmednagar.



Prof. Jyoti. P. Botkar

Botkar Dr.Anita.K.Patil

A TONE

EESA Coordinator

E&TC Department

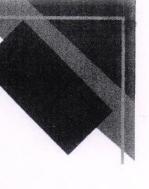
E&TC Department

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Dr. Uady. P. Naik

Principal





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Department of Electronics and Telecommunication

NOTICE

Date: 08/02/2019

All the students are hereby informed that Electronic and Telecommunication Engineering Student Association of our department in association with The Institute of Engineers India is organizing a Three Day Workshop on "Embedded System, Ardiuno and IOT" by our Alumni Er. Akash Dane, Director Synapse System. and Er.Sumedh Saptarshi dated on 21th Feb to 23rd Feb.2019. Those who want to participate can registered their name to student coordinator Sukrut Kulkarni B.E.E&TC as early as possible.

Coordinator

HOD E&TC

Dr. Vithalrao Vikhe Patil College of Engineering, Ahmednagar Department of Electronics & Telecommunication

EESA & IEI

Title: Three day workshop on Embedded System, Ardiuno and IOT

Date: 21/02/2019 to 23/02/2019

Time: 9.30am to 5.00pm

Name od Expert: Er. Akash Dane, Director Synapse Systemes, Ahmednagar

Er. Sumedh Saptarshi, Alumni E&TC

Organization: Synapse System Ahmednagar.

Classes attended: SE & TE students

Participants: 39

Venue: Computer Lab 1(ETC 11)

Event Incharge: Prof. V.A.Dhawale

Student co-ordinator: Sukrut Kulkarni, Pankaj Kokane and Gadhe Shubham.

Objectives:

To get familiar with various concept of Embeded Systems and IOT

To have hands on practice on Ardiuno board.

To learn interfacing of various devices with Ardiuno board.

To explore students with the real world interfacing.

To apply this knowledge in their mini and major project

Outcome:

CO1: Students got the information as well as hands on Ardiuno and Embedded system.

CO2: Student learnt interfacing with sensors, motors and various I/O devices.

CO3: Two Projects build and implemented by students

1.IOT based Smart agriculture System and

2. Smart Stick for Blind.

CO-PO Mapping

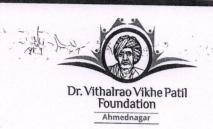
CO	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	P012
CO1	3	3	3	3	3			3	3	3		
CO ₂	3	3	3									
CO3	3	3	3	3				3	3	3		

CO-PSO Mapping

CO	PSO1	PSO2	PSO3
CO1			
CO ₂	3		
CO3	3		

Dr. Vithalrao Vik. Patili College of Engineering Ahmednessi







Report

On

Three Days Workshop By Alumini

Topic: Three Days Workshop on Embedded Systems, Ardiuno and IOT

Date: 21/02/2019 to 23/02/2019

Venue: Department of E&TC - Computer Lab-I (DVVPCOE, Ahmednagar)

Organized by
Department of Electronics and Telecommunication
Dr. Vithalrao Vikhe Patil College of engineering Ahmednagar

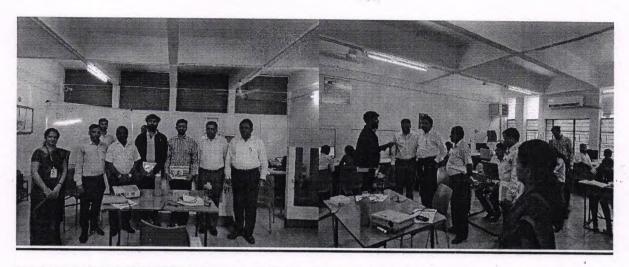
Dr. Vithalrao Vikhe Patil College of Engineering Ahrnednegar Page 1 of

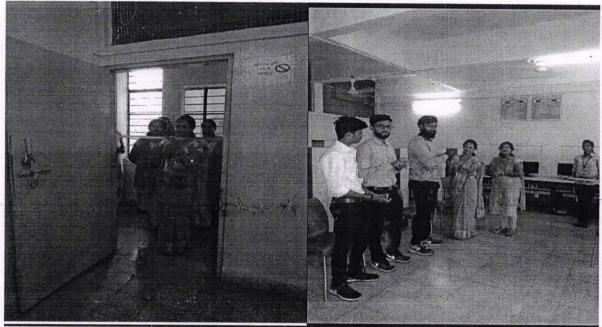
Topics Covered

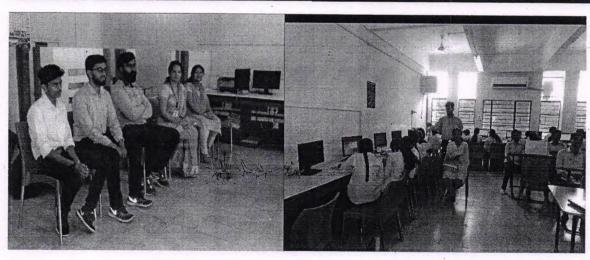
- 1. Introduction to Embedded Systems and Ardiuno.
- 2. Ardiuno IDE Introduction and installation.
- 3. Interfacing with sensors, motors and various I/O devices.
- 4. Communication protocol with Ardiuno.
- 5. IOT (Internet of Things) Introduction, Need, setting up Server, DAS and Data base Management.
- 6. Projects IOT based Smart Agriculture System and
 - Smart Stick for Blind.

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Page 5 of 5

ARDUINO AND IOT WORKSHOP

Group	NAME of STUDENT	Day 1	Day2	Day 3
G1	Nisha Bhau Thorat Sanjana Yezguntala Sushant Wandhekar	Ma Thorse	Layona Schut	Bajag School
G2	Kulkarni Gauri	Carried Continue Vincent	Course Petrini Hiteth	Pedina
G3	Munot Aniket houdham Hinath Ramdas Shinde Avinash Rawsaheb.	Brunot Epselo	Arund 21 Am Balls	Anna Sterio
G4	Suktut Kulkathi Ankita Pamat Layali Mote	Supp Simolo	South	800
G5	Siddhani gaykuman Makesh Shinde Pedris Prakash Banud Akash Hornshchonds	dainas	A STANDS	7) -
G6	Lagh Rupali Gaigre Sonali Goud kontika	Rupau'	Ruprui Boude	Rupcui Boud Floud
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	Shelke Pallavi Roosaheb Gurav Aishwanya Haridaxie Zarrekar Oipali Annavaheb	Acstrosaur	PHONIZ DIRNIZ	Hiswains

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ARDUINO AND IOT WORKSHOP

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G11	chaudhari Vishal Bhalchandra Kokane Pankoj Baljram Gadhe Shubham Bharat	Obline Before Zolle	Giolog Follow	More Ball
G12	Jadhav Anuja Pravin Pathak Aishwanya sunil Sase Pallavi Goratshanath	Anthop Dellarst	Jethah Delon i	Anis. Antak Palleti
G13	Khandagale Mahima Kale Vaishanayi Khalaal Kanchan	NE to Coal	Must be	Make Make
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